

➤ 2.2 Clock Buffers

Buffer is a device allowing the same product to be used across multiple projects and platforms.



Key Features

Ultra-low additive jitter, lower power consumption, support multiple output clock formats from any input clock format includes LVDS、LVPECL、LVCMOS、Crystal.

Applications

Communication Equipment: Switch, Router, MSAN, DSLAM, PON, SONET, SDH device, etc.

Industrial control: High-speed data acquisition card, Servo motor, etc.

Instruments: Signal generator, etc.

Single Output Buffer

Part Number	Output Channels	Output Level	Input Level	Output Voltage	Core Operating Voltage	Additional Jitter	Frequency Range	State
INS6110	10	LVCMOS	LVDS、LVPECL、LVCMOS、Crystal	3.3V、2.5V、1.8V、1.5V	2.5V、3.3V	<50fs	DC~200MHz	●
INS6105	5	LVCMOS	LVDS、LVPECL、LVCMOS、Crystal	3.3V、2.5V、1.8V、1.5V	2.5V、3.3V	<50fs	DC~200MHz	○

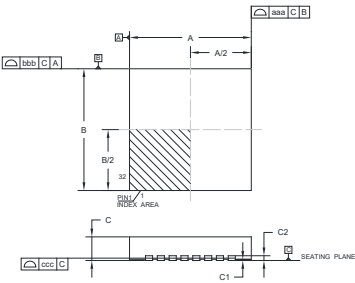
Differential Buffer

Part Number	Output Channels	Output Level	Input Level	Output Voltage	Core Operating Voltage	Additional Jitter	Frequency Range	State
INS6310	10	LVDS、LVPECL、HCSL	LVDS、LVPECL、LVCMOS、Crystal	2.5V、3.3V	2.5V、3.3V	<50fs	DC~3000MHz	○

Configurable Buffer

Part Number	Output Channels	Output Level	Input Level	Output Voltage	Core Operating Voltage	Additional Jitter	Frequency Range	State
INS6234	4	HCSL、LVCMOS	LVDS、LVPECL、LVCMOS、Crystal	2.5V、3.3V	3.3V	<50fs	DC~400MHz	○

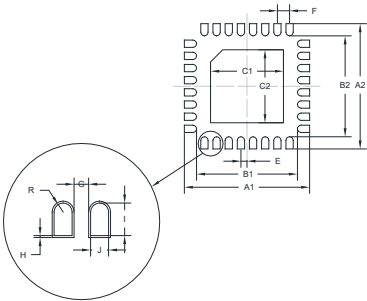
Package



Dimension	Min	Nom	Max
A	4.85	5.00	5.15
B	4.85	5.00	5.15
C	0.80	0.90	1.00
C1	--	--	0.05
C2	--	--	0.20
D	3.05	3.15	3.25
E	2.90	3.00	3.10
F	--	0.50	--
F1	0.30	0.40	0.50
F2	0.18	--	0.30
aaa	--	--	0.15
bbb	--	--	0.10
ccc	--	--	0.08
ddd	--	--	0.10
eee	--	--	0.10

uint:mm

Recommended solder pad:



Dimension	Max
A1	5.15
A2	5.15
B1	4.15
B2	4.15
C1	3.00
C2	3.00
E	0.25
F	0.5
G	0.08
H	0.07
I	0.50
J	0.30

uint:mm